



REPLACEMENT SHEET

FIG. 1

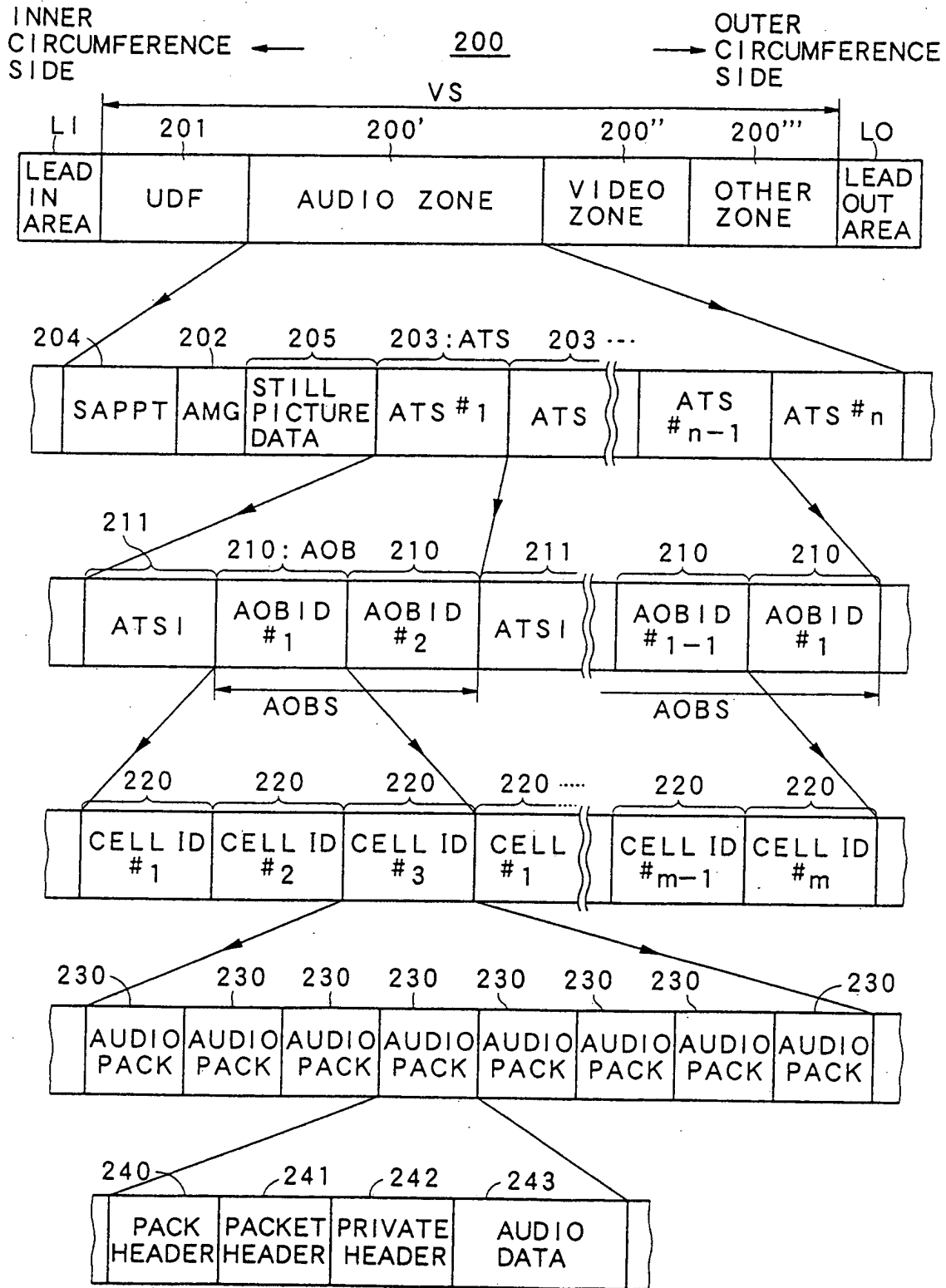




FIG. 2A

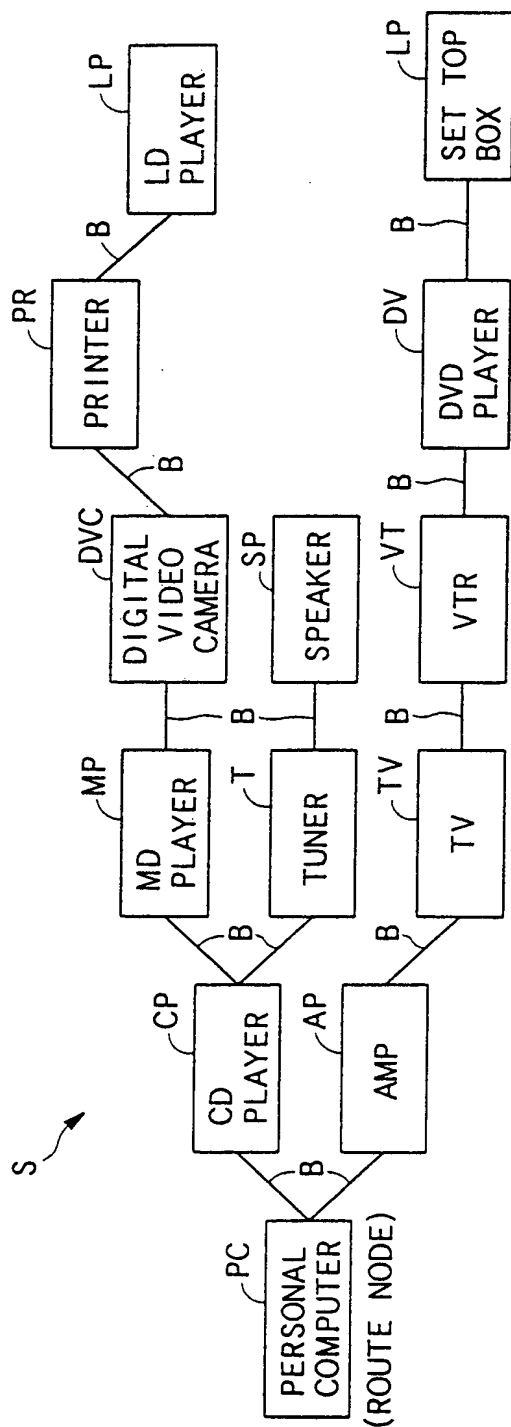
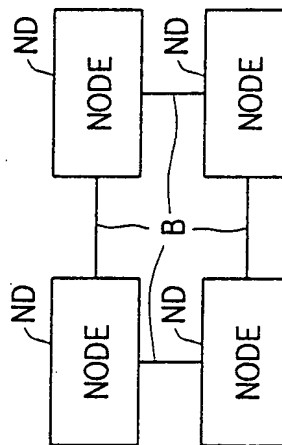


FIG. 2B



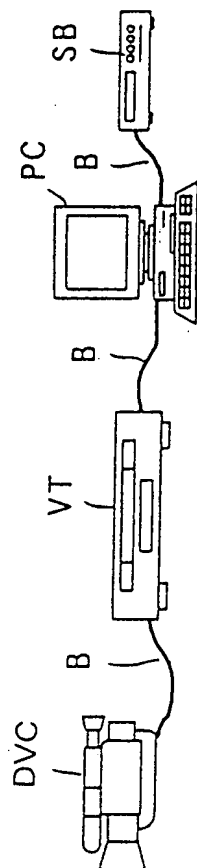


FIG. 3A

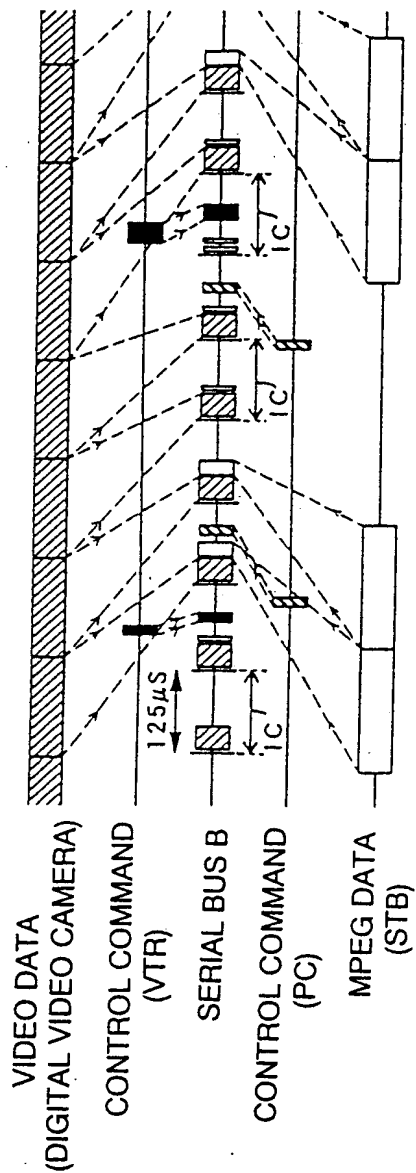


FIG. 3B

FIG. 4

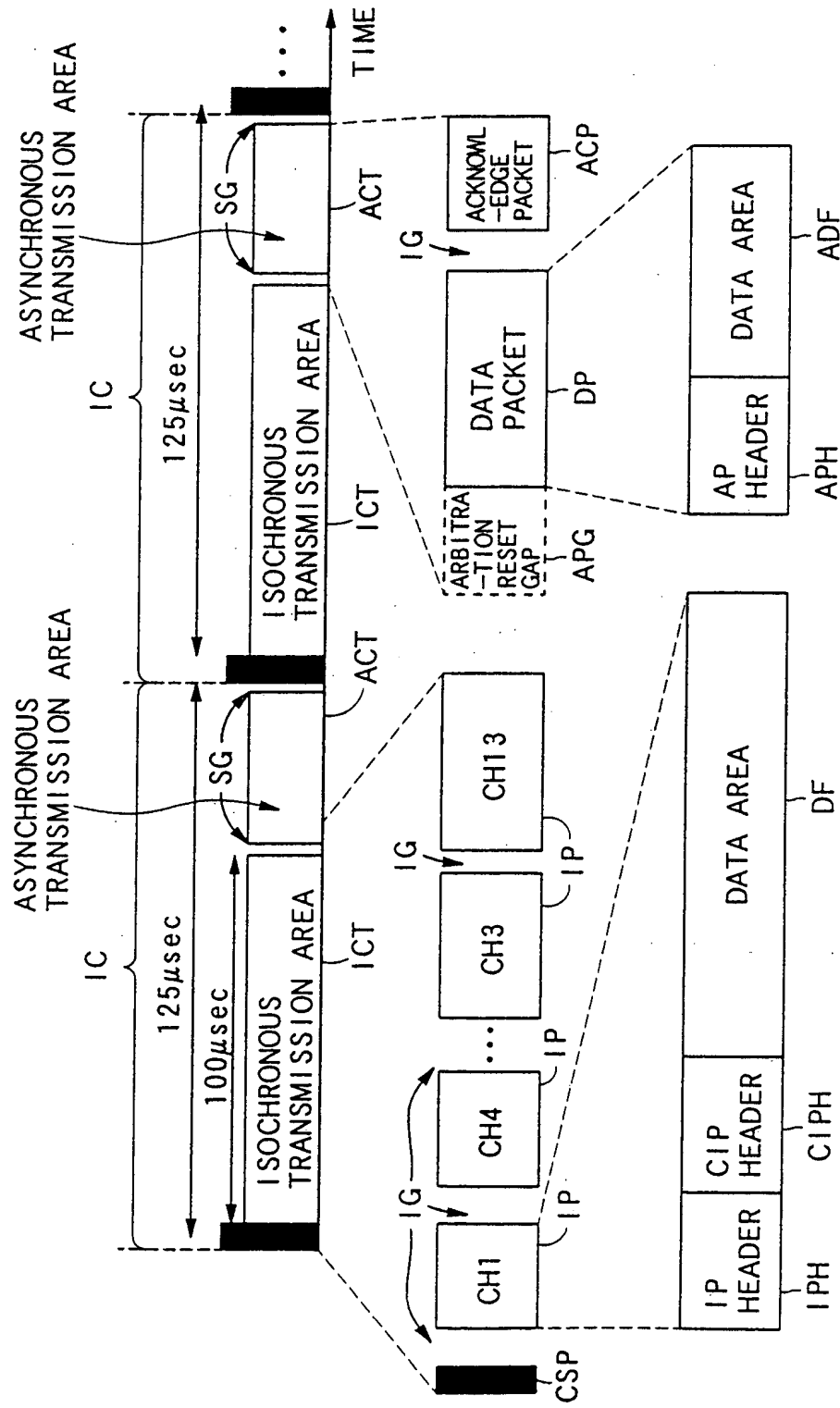


FIG. 5

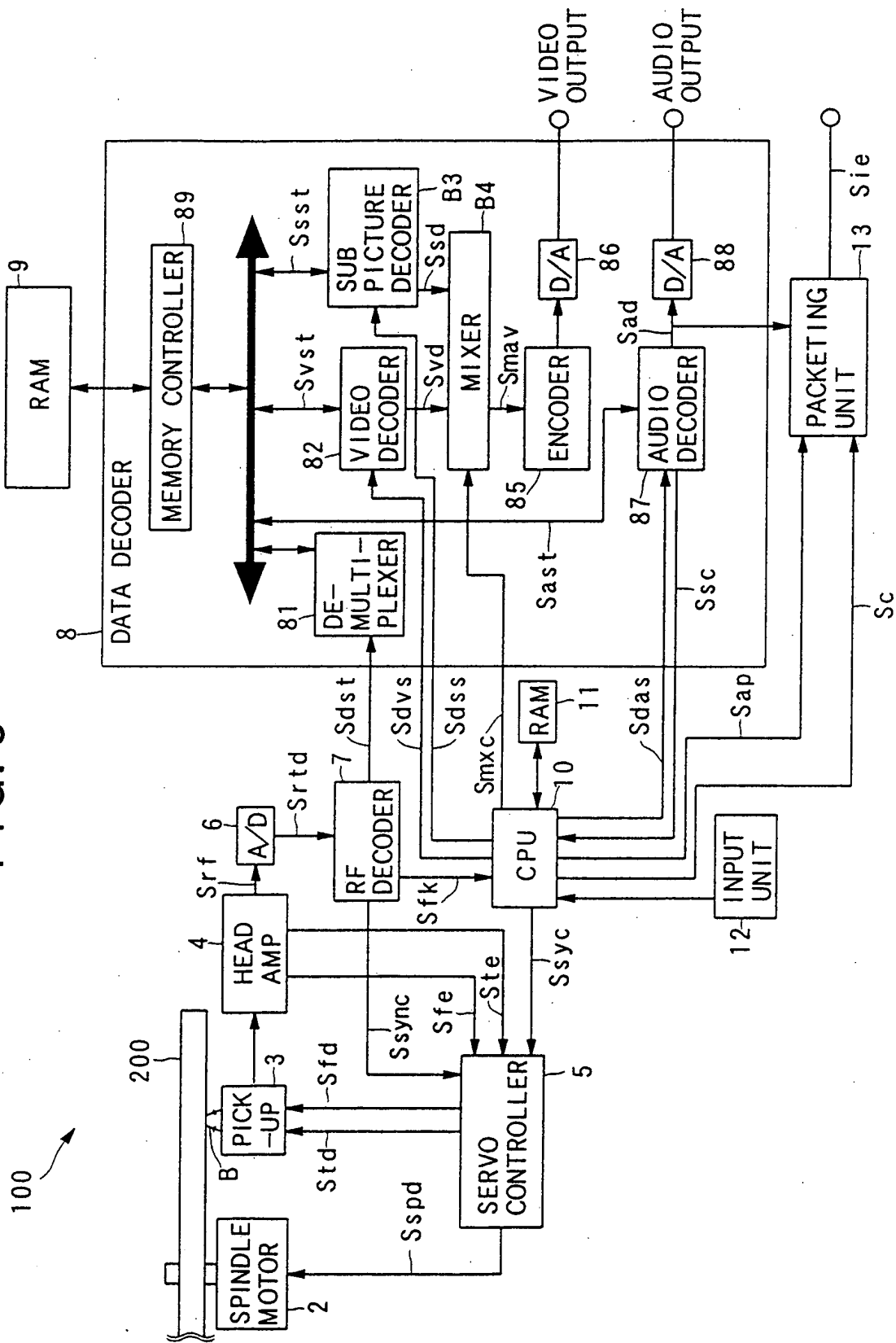




FIG. 6

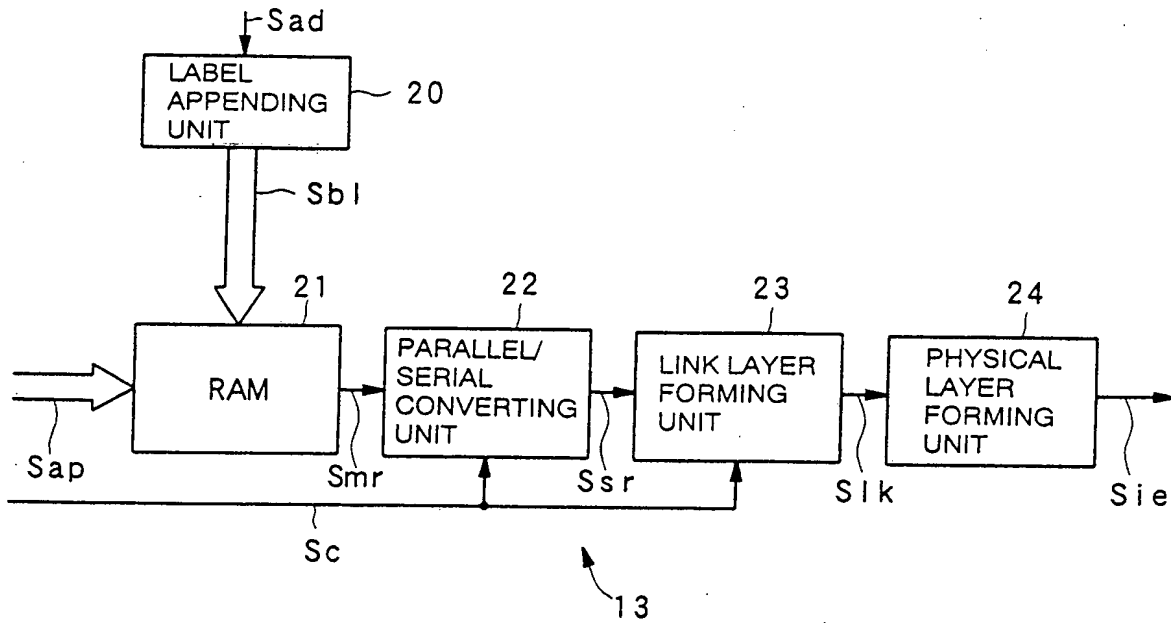




FIG. 7

 $(4 + (n \times 8) + 1)$  ROWS

IP

— <u>IPH</u> —									
— <u>CIPH</u> —									
<u>LB1</u>	<u>AD1</u>								
<u>LB2</u>	<u>AD2</u>								
<u>LB3</u>	<u>AD3</u>								
<u>LB4</u>	<u>AD4</u>								
<u>LB5</u>	<u>AD5</u>								
<u>LB6</u>	<u>AD6</u>								
<u>LB7</u>	<u>DRC</u>	<u>EF</u>	<u>RD</u>	<u>DMM</u>	<u>DMCV</u>	<u>DMC</u>	<u>CCI</u>	<u>RD</u>	
<u>LB8</u>	<u>AR</u>		<u>FS2</u>		<u>MCT</u>	<u>CA</u>	<u>TP</u>	<u>RD</u>	
<u>LB1</u>	<u>AD1</u>								
<u>LB2</u>	<u>AD2</u>								
<u>LB3</u>	<u>AD3</u>								
<u>LB4</u>	<u>AD4</u>								
<u>LB7</u>	<u>DRC</u>	<u>EF</u>	<u>RD</u>	<u>DMM</u>	<u>DMCV</u>	<u>DMC</u>	<u>CCI</u>	<u>RD</u>	
<u>LB8</u>	<u>AR</u>		<u>FS2</u>		<u>MCT</u>	<u>CA</u>	<u>TP</u>	<u>RD</u>	
<u>CRC</u>									

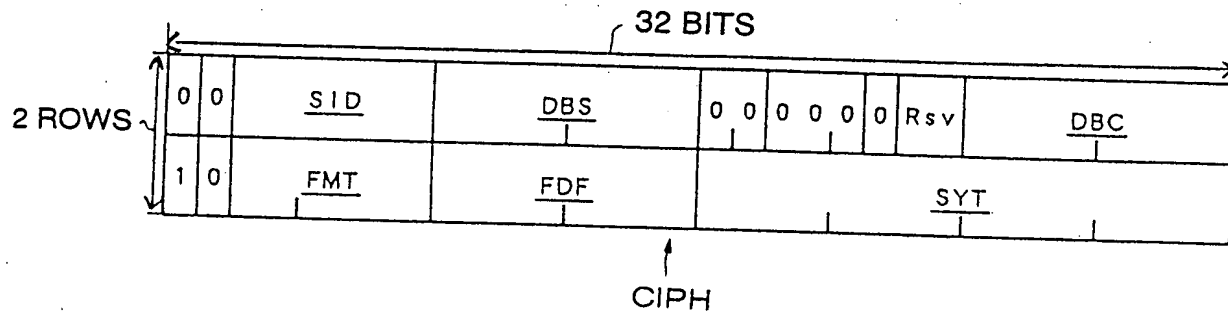
32 BITS

DF



# REPLACEMENT SHEET

## FIG. 8





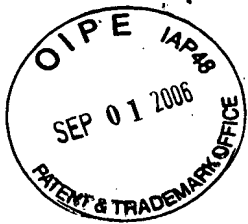


FIG. 9

$(4 + (n \times 8) + 1)$  ROWS

IP

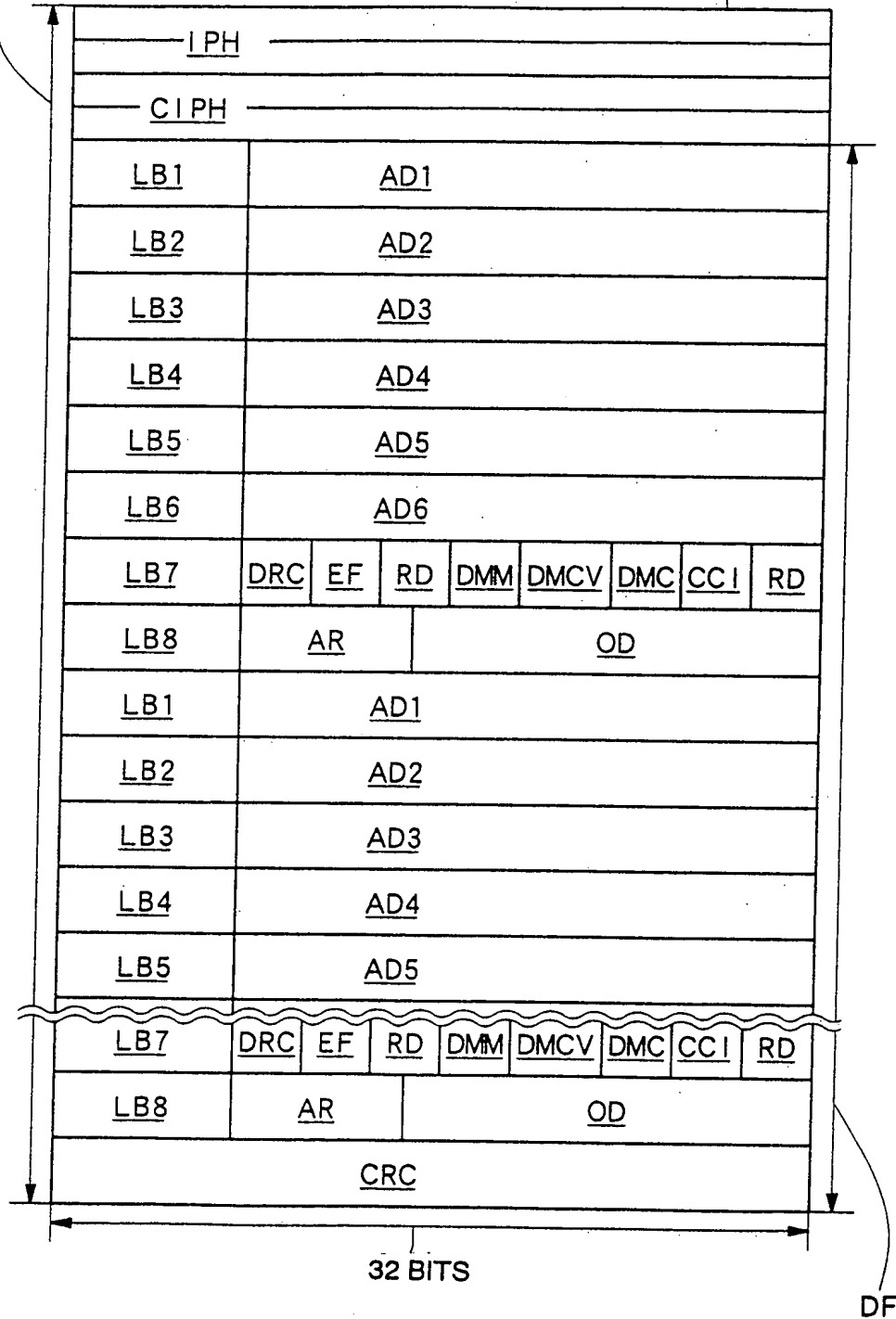


FIG.10

